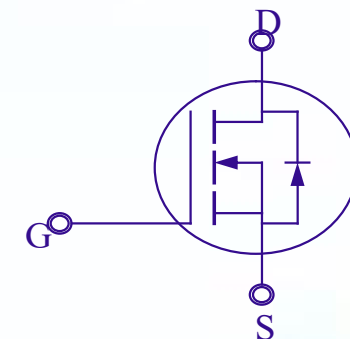




# 華瑞功率電子股份有限公司

## CET-MOS Corporation



# Overview

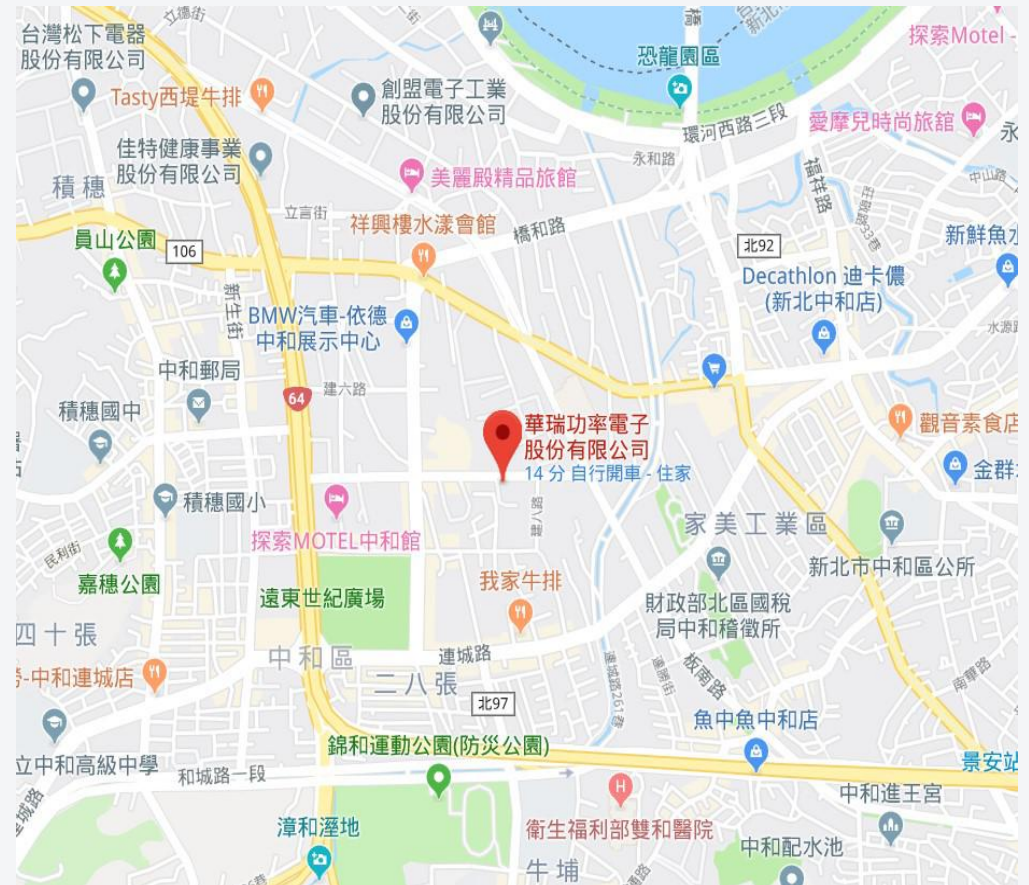
CET-MOS Corporation (CET-MOS), as a spin-off company from Chino-Excel Technology Corporation (CET), inherited the technologies and experiences from CET over 25 years, successfully promote the first MOSFET to Taiwan market and keep as a pioneer in this field !

We are committed to excellence in design and manufacturing high-quality Power MOSFET. CET-MOS provides high performance of variety devices, and also support to the client for ODM or OEM cases with complete total solutions.

## LOCATION :

10F., No. 176, Jiankang Rd.,  
Chung-Ho Dist., New Taipei City, Taiwan.

23585 新北市中和區建康路176號10樓



# Quality System and Green Products

## SERVICE INNOVATION DYNAMIC

TO OFFER BEST QUALITY AND RELIABILITY  
TO THE FULL SATISFACTION OF CUSTOMERS'  
REQUIREMENT IS CET-MOS EVERLASTING QUALITY  
POLICY TO PURSUE.

ASSEMBLY PROCESS	ASSEMBLY TYPE	RoHS							REACH (SVHC)	Non-Halogen
		Pb	Cd	Hg	Cr+6	PBDEs	PBBs	Phthalates (DBP, BBP, DEHP, DIBP)		
Epoxy Process	SOT, SOP, TSOP, TSSOP, DFN...	V	V	V	V	V	V	V	V	V
Solder Process	TO, PPAK...	Pb>1000ppm Exemptions Annex III 7(a)	V	V	V	V	V	V	V	V

DNV-GL

### MANAGEMENT SYSTEM CERTIFICATE

Certificate No: 0966-2003-AQ-40C-RVK Initial certification date: 17 January, 2003 Valid: 17 January, 2018 - 17 January, 2021

This is to certify that the management system of

**CET-MOS Corporation**

10F., No. 176, Jiansong Rd., Chung-Ho Dist., New Taipei City, Taiwan, 23585  
and the sites as mentioned in the appendix accompanying this certificate

has been found to conform to the Quality Management System standard:  
**ISO 9001:2015**

This certificate is valid for the following scope:

**Design and Production Management of Metal Oxide Silicon Field Effect Transistors**

Place and date:  
Shanghai, 12 January, 2018



The RoA is a signature to the IAF MLA

For the issuing office:  
DNV GL - Business Assurance  
Suite A, Building 9, No. 1201 Hongqiao  
Road, Changning District, Shanghai  
200336, P.R. China  
TEL: +86 21 32799000



Zhu Hai Ming  
Management Representative

Lack of fulfillment of conditions as set out in the Certification Agreement may render this Certificate invalid. This certificate shall remain valid only after the certified organization has passed surveillance audit.  
ACCREDITED UNDER DNV GL BUSINESS ASSURANCE N.V., ZWOLLENSE 1, 3906 LB, BALKENBURGH, THE NETHERLANDS. TEL: +31 (0)20 2008500  
www.dnv-gl.com  
THE VALIDITY OF THIS CERTIFICATE CAN BE CHECKED THROUGH THE CNCA WEBSITE IN CHINA: HTTP://WWW.CNCA.GOV.CN

## Background

### CET-MOS Corporation

Chino-Excel Technology Corporation Experience inherited

**Certification:ISO9001:2015**



**1st MOSFETs Design House  
in Taiwan**



**1st MOSFETs Package / new  
structure patent**



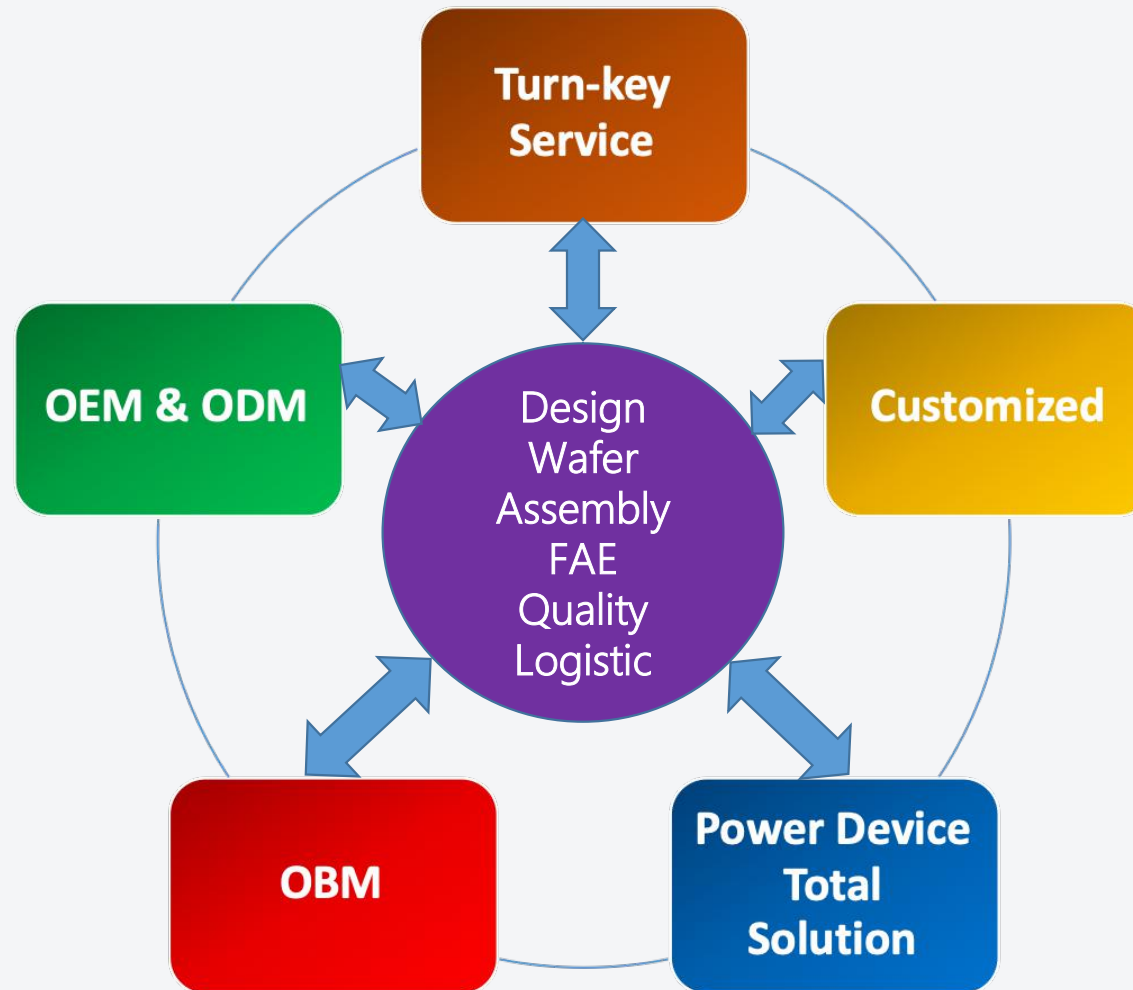
**Broad variety of MOSFETs product  
( -200V~1000V)**



**0.18um Power MOSFETs  
product**

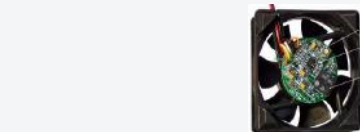


## Business Model





# Application Map



DC Fan



BMS



BLDC



LED TV / Monitor



Adapter



Power tool



Audio AMP



Power inverter



Power bank



SPS



Lighting



Photo



Quick charger



UPS

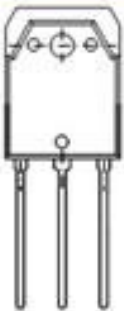















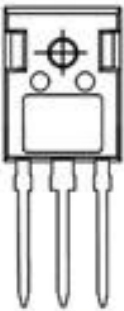



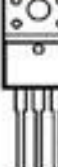



PoE

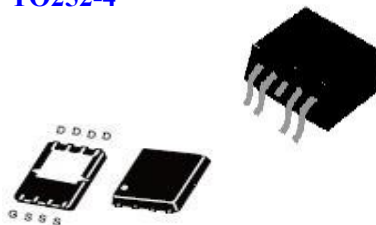


# Series of Package Types

## Available Packages

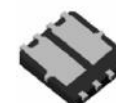
TO-3P	DFN2X2	DFN3X3	PR-PACK5X6	SO-8	TSSOP8
	 2.0X2.0 (2.0X2.0)	 3.0X3.0 (3.0X3.0)	 5.1X6.15 (5.1X5.9)	 5.0X6.20 (5.0X4.06)	 3.1X6.6 (3.1X4.5)
	TSOP6	SOT323	SOT-223	SOT-23(E)	SOT-23
	 3.10X3.0 (3.1X1.8)	 2.2X2.45 (2.2X1.35)	 6.8X7.3 (6.8X3.7)	 3.04X2.95 (3.04X1.70)	 3.04X2.95 (3.04X1.9)
	SOT-89	TO-252	TO-251	TO-92	TO-263
16.1X41.0 (16.1X20.55)					
TO-247					
	4.7X4.5 (4.7X2.69)	7.01X10.6 (7.01X8.41)	7.01X18.08 (7.01X8.43)	4.85X17.55 (4.85X4.85)	10.54X15.88 (10.54X11.05)
	TO-126F	TO-126	TO-262	TO-220F	TO-220
15.90X41.91 (15.90X21.34)					
	8.2X26.95 (8.2X11.2)	8.05X26.85 (8.05X11.1)	10.75X24.6 (10.75X10.93)	10.36X30.75 (10.63X16.3)	10.66X31.24 (10.63X16.51)

▪TO252-4



▪Power pack 5\*6

▪Power Pack 5\*6 (dual)



▪DFN1006

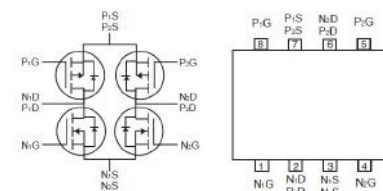


2014

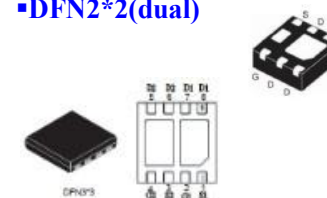
2017

2019

▪SOP-8 x 4 chip



▪DFN2\*2(dual)



▪DFN 3\*3 (dual)

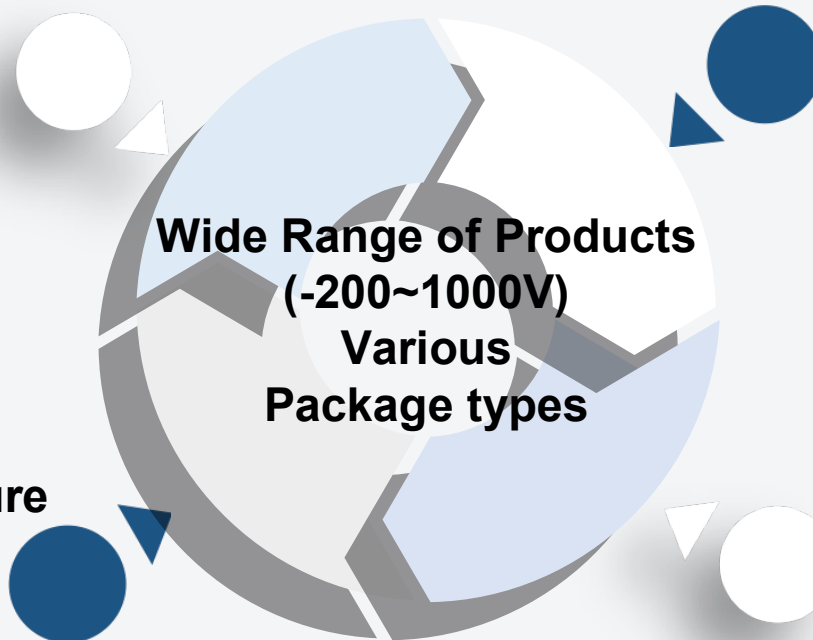
## Advantages

### R&D

Advanced Design  
Customized

### QRA

Short RMA Procedure  
Full Reliability  
Verification



### Foundry Partner

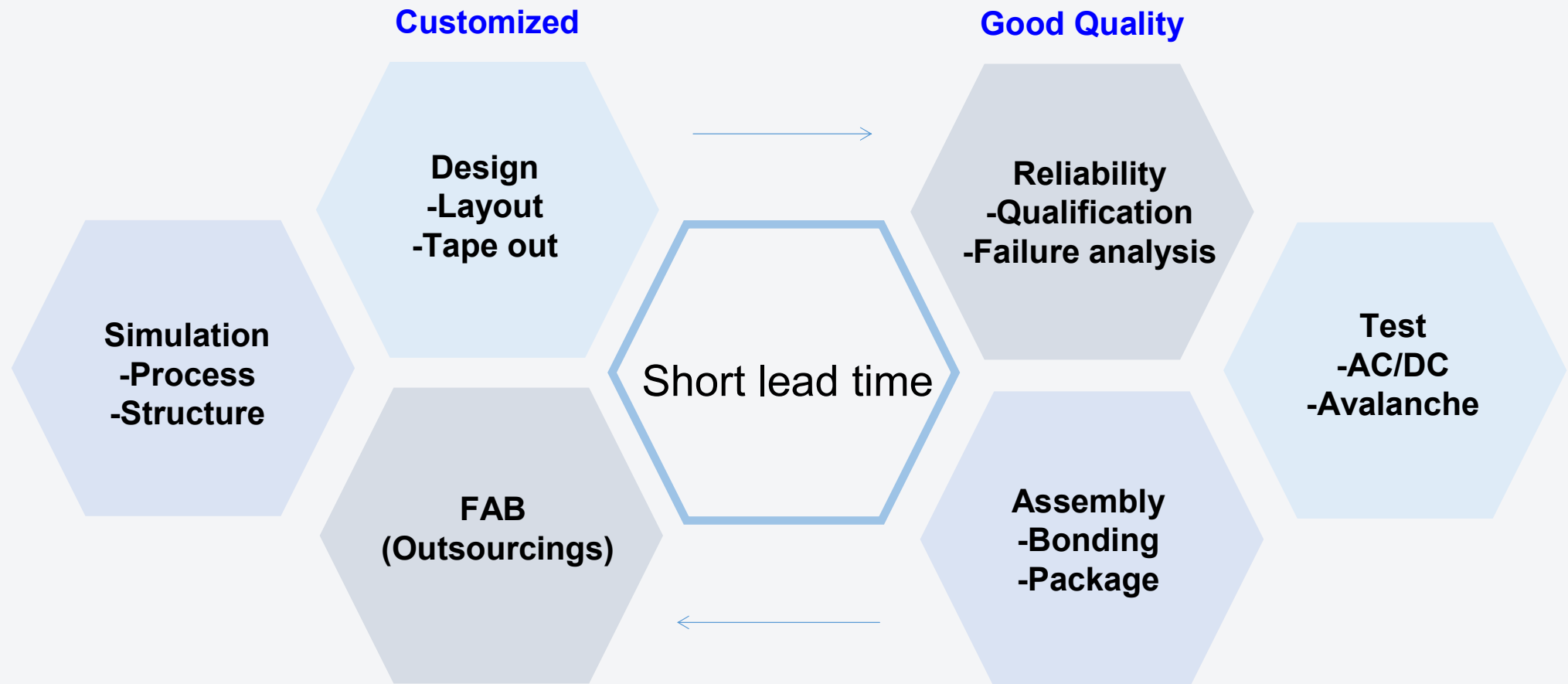
Fine Tune Recipe  
Short Lead Time

### Package

Fine Tune Assembly  
Short Lead Time



## Design Flow



## Why CET-MOS?



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# Thank You