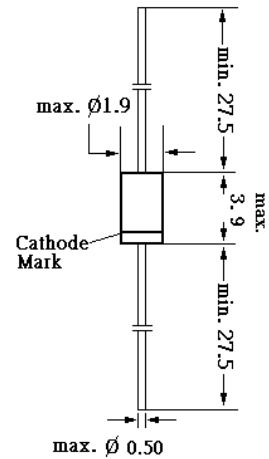


SILICON EPITAXIAL PLANAR DIODE

High Voltage Switching

Features

- High reverse voltage. ($V_R = 250V$)
- High reliability with glass seal.



Glass case JEDEC DO-35
Dimensions in mm

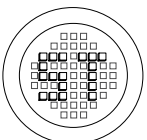
Absolute Maximum Ratings ($T_a = 25^\circ C$)

	Symbol	Value	Unit
Peak reverse voltage	$V_{RM}^{1)}$	300	V
Reverse voltage	V_R	250	V
Peak forward current	I_{FM}	625	mA
Non-Repetitive peak forward surge current	$I_{FSM}^{2)}$	1	A
Average forward current	I_O	200	mA
Power dissipation	P_{tot}	400	mW
Junction temperature	T_j	175	$^\circ C$
Storage temperature	T_s	-65 to +175	$^\circ C$

- Notes: (1) Reverse voltage in excess of peak reverse voltage may deteriorate electrical characteristic.
(2) Within 1s forward surge current.

Characteristics at $T_a = 25^\circ C$

	Symbol	Min.	Typ.	Max.	Unit
Forward voltage at $I_F = 100mA$	V_F	-	-	1	V
Reverse current at $V_R = 250V$	I_{R1}	-	-	0.2	μA
at $V_R = 300V$	I_{R2}	-	-	100	μA
Capacitance at $f = 1MHz$	C	-	1.5	-	pF
Reverse recovery time at $I_F = I_R = 30mA$, $I_{rr} = 3mA$, $R_L = 100\Omega$	t_{rr}	-	-	100	ns



®

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